

The image shows a 4x4 grid of binary patterns. The patterns are as follows:

- Top-left cell: SSSS
- Top-middle cell: YYY
- Top-right cell: YYY
- Bottom-right cell: SSSS
- Middle-left cell: SSS
- Middle-middle cell: SSS
- Middle-right cell: SSS
- Bottom-right cell: SSS

FILEID**SYSIMGFIX

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```
0000 1 .TITLE SYSSIMGFI - Address Fixup System Service
0000 2 .IDENT 'V04-000'
0000 3 :*****
0000 4 :*****
0000 5 :*
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0000 23 :*
0000 24 :*
0000 25 :*****
0000 26 :*
0000 27 :++
0000 28 : Facility:
0000 29 :
0000 30 : Executive - Image Activator Completion Routines
0000 31 :
0000 32 : Abstract:
0000 33 :
0000 34 : This module contains subroutines used by the image activator
0000 35 : to perform address relocation after images have been activated.
0000 36 :
0000 37 : Environment:
0000 38 :
0000 39 : Most of the code in this module runs in user mode but some routines
0000 40 : may also be called from exec mode.
0000 41 :
0000 42 : .SUBTITLE History
0000 43 :
0000 44 : Author:
0000 45 :
0000 46 : Lawrence J. Kenah
0000 47 :
0000 48 : Creation Date:
0000 49 :
0000 50 : 19 March 1984
0000 51 :
0000 52 : Modified by:
0000 53 :
0000 54 : V03-010 LJK0279 Lawrence J. Kenah 8-May-1984
0000 55 : Miscellaneous cleanup. Remove temporary definition of
0000 56 : SHL$B_SHL_SIZE. Put all code into YF$SYSIMGACT program
0000 57 : section.
```

0000	58	:
0000	59	:
0000	60	:
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0000	67	:
0000	68	:
0000	69	:
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0000	98	:
0000	99	--

V03-009 LJK0270 Lawrence J. Kenah 31-Mar-1984
Add code to call shareable image initialization routines.

V03-008 LJK0275 Lawrence J. Kenah 25-Mar-1984
The size of SHL elements is variable. It depends on when
the image was linked.

V03-007 LJK0238 Lawrence J. Kenah 26-Jul-1983
Use new concept of image base address instead of first address
into which image is mapped.

V03-006 LJK0218 Lawrence J. Kenah 28-Jun-1983
Minor cleanup.

V03-005 LJK0200 Lawrence J. Kenah 14-Jun-1983
Make changes that support new image activator

Base addresses of shareable images are now located by searching
the ICB list, a much simpler list than the master fixup vector
list. Routine COPY_SHL is no longer needed. All code that
existed to support a previous design for mapping shareable
images permanently into P1 space is also eliminated. Use
IMG\$ prefix for global entry point names. Eliminate prefix
from routines that are local.

V03-004 LJK0195 Lawrence J. Kenah 9-Mar-1983
Make so-called recursive activation capable of activating
more than one image without dropping some fixups on the floor.

V03-003 LJK0192 Lawrence J. Kenah 7-Jan-1983
Do poor man's recursive activation to support shareable
images that reference other shareable images not known
to the image header of the executable image.

V03-002 MLJ0099 Martin L. Jack, 20-Oct-1982 19:40
Fix broken BSBWs.

V03-001 KDM0002 Kathleen D. Morse 28-Jun-1982
Added \$SSDEF.

0000	101	.SUBTITLE	Declarations
0000	102		
0000	103	: Include Files:	
0000	104		
0000	105	\$IACDEF	: Image activator control flags
0000	106	\$IAFDEF	: Offsets into image activator fixup
0000	107		area within image file
0000	108	\$ICBDEF	: Image control block offsets
0000	109	\$IMAGCTXDEF	: Context of currently executing image
0000	110	\$IMGDEF	: Image activator status codes
0000	111	\$PSLDEF	: PSL field definitions and constants
0000	112	\$SETPRTDEF	: Argument list offsets for \$SETPRT system service
0000	113	\$SHLDEF	: Offsets into shareable image list element

0000 115 .SBTTL EXESIMGFI Address Relocation Fixup System Service
 0000 116 :+
 0000 117 : Functional Description:
 0000 118 :
 0000 119 : This procedure is called after an image is activated but before it
 0000 120 : is called in order to allow address fixups to be performed in user
 0000 121 : access mode if the caller of the image so wishes. This prevents
 0000 122 : process destruction or security breaches if the image that has just
 0000 123 : been activated contains garbage or selected cleverness.
 0000 124 :
 0000 125 : Calling Sequence:
 0000 126 :
 0000 127 : CALLS #0,EXESIMGFI
 0000 128 :
 0000 129 : Implicit Input:
 0000 130 :
 0000 131 : Address space of image just activated that contains fixup vectors
 0000 132 : that describe the address fixups that must be done.
 0000 133 :
 0000 134 : Implicit Output:
 0000 135 :
 0000 136 : All appropriate locations are relocated to reflect the locations
 0000 137 : of each shareable image during this activation of the image.
 0000 138 :
 0000 139 : Completion Codes:
 0000 140 :
 0000 141 : R0 low bit set => successful completion (SS\$_NORMAL)
 0000 142 :
 0000 143 : R0 low bit clear => error occurred
 0000 144 :
 0000 145 : Actual error status codes are returned by GET_BASE_ADDRESSES and
 0000 146 : PROCESS_FIXUP_LIST.
 0000 147 :
 0000 148 : Side Effects:
 0000 149 :
 0000 150 : See the routine headers for the two functional routines for a
 0000 151 : description of the effects of this procedure.
 0000 152 :-
 0000 153 :
 0000 154 : Put all of this module into a separate pageable program section
 0000 155 :
 0000 156 .PSECT YFSSYSIMGACT
 0000 157 :
 0000 158 EXESIMGFI:::
 35 003C 0000 159 .WORD "MCR2,R3,R4,R5" : Save some registers
 26 50 E9 0002 160 BSBB GET_BASE_ADDRESSES : Get base address of each image
 00AC 30 0004 161 BLBC R0,T0\$: Skip hard part if an error occurred
 10 E1 0007 162 BSBW PROCESS_FIXUP_LIST : Do the actual relocation
 1B 00000000'GF 000A 163 BBC #IMAGCTX\$V SETVECTOR,-
 000C 164 G^IAC\$GL IMAGCTX,10\$: Any vectors to set?
 0012 165 SIMGACT_S 0,0,0,-
 11 E1 002D 166 IMGCTL=#IAC\$M SETVECTOR : Let image activator set them
 03 00000000'GF 002F 167 10\$: BBC #IMAGCTX\$V INITIALIZE,-
 0189 30 0035 168 G^IAC\$GL IMAGCTX,20\$: Any routines to be called?
 04 0038 169 BSBW INISHRIMG : Find them and call them
 170 20\$: RET : Return with final status

0039 172 .SBTTL GET_BASE_ADDRESSES - Locate Each Shareable Image
 0039 173 :+
 0039 174 : Functional Description:
 0039 175 :
 0039 176 : This routine is called before the actual fixup operations are performed
 0039 177 : to determine the base address of each shareable image that has been
 0039 178 : mapped. If a shareable image in the fixup list has no corresponding
 0039 179 : entry of the same name in the master ICB list, an error is reported.
 0039 180 :
 0039 181 : Note that the image activator has filled in the base address for SHL
 0039 182 : entry 0, the SHL associated with the image itself.
 0039 183 :
 0039 184 : Calling Sequence:
 0039 185 :
 0039 186 : JSB GET_BASE_ADDRESSES
 0039 187 :
 0039 188 : Input Parameters:
 0039 189 :
 0039 190 : none
 0039 191 :
 0039 192 : Implicit Input:
 0039 193 :
 0039 194 : Listheads for fixup vector list and ICB list
 0039 195 :
 0039 196 : Output Parameters:
 0039 197 :
 0039 198 : none
 0039 199 :
 0039 200 : Implicit Output:
 0039 201 :
 0039 202 : All SHL entries in the linked list of fixup vectors have base addresses
 0039 203 : of their associated shareable images stored in SHL\$L_BASEVA.
 0039 204 :
 0039 205 : Completion Codes:
 0039 206 :
 0039 207 : R0 = SSS_NORMAL
 0039 208 :
 0039 209 : All base addresses were successfully stored.
 0039 210 :
 0039 211 : R0 = IMGS_IMAGE_NOT_FOUND
 0039 212 :
 0039 213 : A shareable image name in a SHL entry had no corresponding
 0039 214 : ICB. This means that the shareable image was not mapped,
 0039 215 : which indicates an inconsistency between SHL entries and
 0039 216 : image section descriptors in the image header of one of the
 0039 217 : images that was mapped.
 0039 218 :
 0039 219 : Side Effects:
 0039 220 :
 0039 221 : R0 and R1 are destroyed
 0039 222 :
 0039 223 :
 0039 224 : GET_BASE_ADDRESSES:
 55 FFFFFFFC'GF DE 0039 225 : R0VAL G^<CTL\$GL_FIXUPLNK-IAF\$L_FIXUPLNK>,R5 ; Pick up listhead address
 0040 226 :
 55 04 A5 D0 0040 227 10\$: MOVL IAF\$L_FIXUPLNK(R5),R5 ; Get address of next fixup vector
 27 13 0044 228 BEQL 30\$; Return success if done

SYSSIMGF IX
V04-000

- Address Fixup System Service 16-SEP-1984 02:20:23 VAX/VMS Macro V04-00
GET_BASE_ADDRESSES - Locate Each Shareab 5-SEP-1984 03:54:43 [SYS.SRC]SYSIMGFIX.MAR:1

C 1

16-SEP-1984 02:20:23 VAX/VMS Macro V04-00
5-SEP-1984 03:54:43 [SYS.SRC]SYSIMGFIX.MAR:1

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(4)

52	1C A5	D0	0046	229	MOVL	IAFSL_SHRIMGCNT(R5),R2	; Count of SHL entries to R2	
53	18 A5	F4	13	004A	230	BEQL	10\$	
	55	C1	004C	231	ADDL3	R5,IAFSL_SHLSTOFF(R5),R3	; None here. Get next fixup vector	
	54	10 A3	9A	0051	232	MOVZBL	SHL\$B_SIZE(R3),R4	; Address of first SHL entry to R3
				0055	233		Get size of each SHL element	
				0055	234		: By jumping into the middle of the loop, we are in effect skipping over	
				0055	235		: entry 0, whose base address was stored by the image activator when the	
				0055	236		: image was mapped.	
				0055	237			
		OE	11	0055	238	BRB	25\$	
				0057	239			
50	18 A3	9E	0057	240	20\$: MOVAB	SHL\$T_IMGNAM(R3),R0	; Pass shareable image name in R0	
	0015	30	0058	241	BSBW	IMG\$IS_IT_MAPPED	; Find associated SHL entry in ICB LIST	
	11 50	E9	005E	242	BLBC	R0,40\$; Quit if error occurred	
63	5C A1	D0	0061	243	MOVL	ICBSL_BASE_ADDRESS(R1),SHL\$L_BASEVA(R3)		
			0065	244			; Store base address	
53	54	C0	0065	245	25\$: ADDL2	R4,R3	; Point to next SHL entry	
	EC 52	F5	0068	246	SOBGTR	R2, 20\$; and do next entry	
			006B	247				
		D3	11	006B	248	BRB	10\$; Go back and get next fixup vector
			006D	249				
50	0000'8F	3C	006D	250	30\$: MOVZWL	#SSS_NORMAL,R0	; Indicate success to caller	
		05	0072	251	40\$: RSB		; and return	

0073 253 .SUBTITLE IMGSIS_IT_MAPPED - Search ICB List for Shareable Image
 0073 254 ::+
 0073 255 Functional Description:
 0073 256 This routine searches the shareable image list associated with the
 0073 257 executable image to determine whether a shareable image with a given
 0073 258 name exists in the list. This routine is used to determine whether a
 0073 259 shareable image has already been mapped. It is also used to relate the
 0073 260 relative shareable image list within a shareable image to the shareable
 0073 261 image list associated with the executable image.
 0073 262
 0073 263 Calling Sequence:
 0073 264 JSB IMGSIS_IT_MAPPED
 0073 265
 0073 266 Input Parameters:
 0073 267
 0073 268 R0 = address of counted (ASCII) string of shareable image name
 0073 269
 0073 270 Implicit Input:
 0073 271
 0073 272 IAC\$GL_IMAGE_LIST - Doubly linked list of ICBs describing images
 0073 273 that have already been mapped
 0073 274
 0073 275 Output Parameters:
 0073 276
 0073 277 If successful, R1 contains the address of the image control block
 0073 278 that describes the named image.
 0073 279
 0073 280 Completion Codes:
 0073 281
 0073 282 R0 low bit set indicates success (SSS_NORMAL)
 0073 283
 0073 284 R0 low bit clear indicates failure (IMGS_IMAGE_NOT_FOUND)
 0073 285
 0073 286 This status indicates that no match occurred, implying that
 0073 287 the shareable image in question has not yet been mapped.
 0073 288
 0073 289 The severity of this status depends on the caller. This routine
 0073 290 is called by the image activator to determine whether an image
 0073 291 has been mapped. If the image name is not found, then the image
 0073 292 activator maps the image. If this routine returns this status
 0073 293 to the fixup code located earlier in this module, that is a
 0073 294 fatal error indicating an inconsistency between shareable image
 0073 295 lists inside fixup vectors and ISD lists in image headers.
 0073 296
 0073 297
 0073 298 :-
 0073 299
 0073 300 IMGSIS_IT_MAPPED:::
 00FC 8F BB 0073 301 P0SHR #^M<R2,R3,R4,R5,R6,R7> : Save some registers
 54 80 9A 0077 302 MOVZBL (R0)+,R4 : Save character count in R4
 55 50 D0 007A 303 MOVL R0,R5 : Save string address in R5
 007D 304
 007D 305 ASSUME ICB\$L_FLINK EQ 0
 007D 306
 57 00000000'GF DE 007D 307 MOVAL G^IAC\$GL_IMAGE_LIST,R7 : Get address of ICB listhead
 56 57 DO 0084 308 MOVL R7,R6 : Copy it to a working register
 0087 309

56	66	D0	0087	310	10\$:	MOVL	ICBSL_FLINK(R6),R6	: Get address of next ICB
57	56	D1	008A	311		CMPB	R6 R7	: Check for end of list
	1C	13	008D	312		BNEQ	30\$: Equality indicates no more ICBs
14	A6	54	91	008F	313	CMPB	R4 ICBST_IMAGE_NAME(R6)	; Do string sizes agree?
	F2	12	0093	314		BNEQ	10\$; No, go get next ICB
15	A6	65	54	29	0095	CMPC3	R4 (R5),ICBST_IMAGE_NAME+1(R6)	; Check strings for equality
	EB	12	009A	315		BNEQ	10\$; Go get next ICB if no match
51	56	D0	009C	316		MOVL	R6,R1	; Store ICB address
50	00000000'8F	D0	009F	317		MOVL	#SSS_NORMAL_R0	: Indicate success to caller
00FC 8F	BA	00A6	318	320	20\$:	POPR	#^M<R2,R3,R4,R5,R6,R7>	; Restore registers
	05	00AA	319	321		RSB		; and return
	00AB	322		323				
	00AB	324						: If we loop through the entire ICB list without matching the image name, then
	00AB	325						: the shareable image has not yet been mapped. Indicate that to caller.
50	084D8962 8F	D0	00AB	326	30\$:	MOVL	#IMGS_IMAGE_NOT_FOUND,R0	
	51	D4	00B2	327		CLRL	R1	
	F0	11	00B4	328		BRB	20\$	
			329					

00B6 331 .SBTTL PROCESS_FIXUP_LIST - Perform Post-Activation Fixups
00B6 332 :+
00B6 333 : Functional Description:
00B6 334 :
00B6 335 : This routine processes a linked list of fixup vectors and performs
00B6 336 : the specific fixup operations listed in each vector. There are three
00B6 337 : forms of fixup.
00B6 338 :
00B6 339 :
00B6 340 :
00B6 341 :
00B6 342 :
00B6 343 :
00B6 344 :
00B6 345 :
00B6 346 :
00B6 347 :
00B6 348 : Calling Sequence:
00B6 349 :
00B6 350 : JSB PROCESS_FIXUP_LIST
00B6 351 :
00B6 352 : Input Parameters:
00B6 353 :
00B6 354 : none
00B6 355 :
00B6 356 : Implicit Input:
00B6 357 :
00B6 358 : CTL\$GL_FIXUPLNK Listhead of linked list of fixup vectors for
00B6 359 : a set of shareable images
00B6 360 :
00B6 361 : Output Parameters:
00B6 362 :
00B6 363 :
00B6 364 :
00B6 365 :
00B6 366 :
00B6 367 :
00B6 368 : Elements in fixup vector G-hat offset area have base address
00B6 369 : of appropriate shareable image added to them.
00B6 370 :
00B6 371 : .ADDRESS directives throughout the address space have base
00B6 372 : addresses added in.
00B6 373 :
00B6 374 : Pages that should eventually be read-only but were set to
00B6 375 : writable while the image activator works are set back to read-only.
00B6 376 : Completion Codes:
00B6 377 :
00B6 378 :
00B6 379 :
00B6 380 : Side Effects:
00B6 381 :
00B6 382 : CTL\$GL_FIXUPLNK cleared after fixups are completed.
00B6 383 :
00B6 384 :--
00B6 385 :
00B6 386 : PROCESS_FIXUP_LIST:
00B6 387 : MOVAL G^<CTL\$GL_FIXUPLNK-IAFSL_FIXUPLNK>,R5 ; Pick up listhead address

55 FFFFFFFC'GF DE 00B6 387

00F2 410 .SBTTL FIXUP_G_HAT Fixup G-hat exit vector
 00F2 411 :+
 00F2 412 : Functional Description:
 00F2 413 :
 00F2 414 : This routine performs the G-hat fixup for a specific exit vector.
 00F2 415 : specifically, the base address of the appropriate shareable image
 00F2 416 : is added to each entry in the exit vector.
 00F2 417 :
 00F2 418 : Calling Sequence:
 00F2 419 :
 00F2 420 BSBW FIXUP_G_HAT
 00F2 421 :
 00F2 422 : Input Parameters:
 00F2 423 :
 00F2 424 : R4 = Address of G-hat fixup area within fixup vector
 00F2 425 :
 00F2 426 : Implicit Input:
 00F2 427 :
 00F2 428 : Contents of G-hat fixup area
 00F2 429 :
 00F2 430 : Output Parameters:
 00F2 431 :
 00F2 432 : none
 00F2 433 :
 00F2 434 : Implicit Output:
 00F2 435 :
 00F2 436 : Elements in fixup vector G-hat offset area have base address
 00F2 437 : of appropriate shareable image added to them.
 00F2 438 :
 00F2 439 : Completion Codes:
 00F2 440 :
 00F2 441 : none
 00F2 442 :
 00F2 443 : Side Effects:
 00F2 444 :
 00F2 445 : R0, R1, and R2 are destroyed
 00F2 446 :-
 00F2 447 :
 00F2 448 FIXUP_G_HAT:
 52 84 D0 00F2 449 MOVL (R4)+,R2 : R2 contains a count of fixups
 0D 13 00F5 450 BEQL 20\$: A zero indicates the end of the G-hat data
 51 84 D0 00F7 451 MOVL (R4)+,R1 : Store shareable image number in R1
 0E 10 00FA 452 BSBB SHIMG_BASVA : and then load R1 with base address
 00FC 453 : of next shareable image.
 84 51 C0 00FC 454 10\$: ADDL2 R1,(R4)+ : Bias next exit vector entry
 FA 52 F5 00FF 455 SOBGTR R2,10\$: Do next entry
 EE 11 0102 456 BRB FIXUP_G_HAT : Now do next shareable image
 0104 457 :
 0104 458 20\$: MOVZWL #SSS_NORMAL,R0 : Indicate success
 0000'8F 3C 0104 459 RSB : Return
 05 0109 :

010A 461 .SBTTL SHIMG_BASVA Convert a shareable image index to an address

010A 462 :+ Functional Description:

010A 464 :
010A 465 This routine converts a relative shareable image number into the
010A 466 absolute base address at which that shareable image is mapped. It
010A 467 assumes that the base address of each shareable image has already
010A 468 been stored in its associated SHL entry.

010A 469 :
010A 470 Calling Sequence:
010A 471 :
010A 472 BSBW SHIMG_BASVA
010A 473 :
010A 474 Input Parameters:
010A 475 :
010A 476 R1 = Relative number of shareable image
010A 477 R5 = Base address of fixup vector
010A 478 :
010A 479 : Implicit Input:
010A 480 :
010A 481 : Contents of SHLSL_BASEVA for shareable image indexed by R1.
010A 482 :
010A 483 : Output Parameters:
010A 484 :
010A 485 R1 = Base address of shareable image indicated by input parameter
010A 486 :
010A 487 : Side Effects:
010A 488 :
010A 489 R0 is destroyed
010A 490 :
010A 491 :
010A 492 SHIMG_BASVA:
50 18 A5 55 C1 010A 493 ADDL3 R5,IAFSL_SHLSTOFF(R5),R0 ; Base address of shareable image list
50 7E 10 A0 9A 010F 494 MOVZBL SHLSB_SIZE(R0),-(SP) ; Get size of each SHL element
50 50 51 8E 7A 0113 495 EMUL (SP)+,R1,R0,R0 ; R0 points to correct SHL entry
51 60 D0 0118 496 MOVL SHLSL_BASEVA(R0),R1 ; Store associated base address
05 011B 497 RSB ; and return

011C 499 .SBTTL FIXUP_ADDRESS Fixup .ADDRESS entries throughout the image

011C 500 ;+ Functional Description:

011C 501 This routine performs the .ADDRESS fixup for a specific exit vector.
011C 502 Specifically, the base address of the appropriate shareable image
011C 503 is added to each .ADDRESS entry in this shareable image.

011C 504 Calling Sequence:

011C 505 BSBW FIXUP_ADDRESS

011C 506 Input Parameters:

011C 507 R3 = Base address of shareable image whose .ADDRESS directives
011C 508 are being fixed
011C 509 R4 = Address of .ADDRESS fixup area within fixup vector

011C 510 Implicit Input:

011C 511 Contents of .ADDRESS fixup area

011C 512 Implicit Output:

011C 513 .ADDRESS directives within this shareable image have the base addresses
011C 514 of the appropriate shareable images added to them.

011C 515 ;-

011C 516 FIXUP_ADDRESS:

52	84	D0	011C 517	MOVL (R4)+,R2	: R2 contains a count of fixups
	11	13	011F 518	BEQL 20\$: A zero indicates the end of the G-hat data
51	84	D0	0121 519	MOVL (R4)+,R1	: Store shareable image number in R1
	E4	10	0124 520	BSBW SHIMG_BASVA	: and then load R1 with base address
			0126 521		: of next shareable image.
50	84	C1	0126 522	ADDL3 R3,(R4)+,R0	: Get address of .ADDRESS directive
	60	51	012A 523	ADDL2 R1,(R0)	: Bias by base address of shareable image
F6	52	F5	012D 524	S0BGTR R2,10\$: Do next entry
	EA	11	0130 525	BRB FIXUP_ADDRESS	: Now do next shareable image
50	0000'8F	3C	0132 526	MOVZWL #SSS_NORMAL,R0	: Indicate success
		05	0137 527	RSB	: Return

0138 541 .SBTTL FIXUP_PROT Alter page protection to read only
 0138 542 :+
 0138 543 Functional Description:
 0138 544
 0138 545 This routine alters the page protection of various sections within
 0138 546 the image to read only. These pages were initially writable so the
 0138 547 image activator could fixup all of the relative references. The pages
 0138 548 cannot be writable while the image is executing.

Calling Sequence:

BSBW FIXUP_PROT

Input Parameters:

R3 = Base address of image whose pages' protection is being altered
 R4 = Address of protection data within fixup vector

Implicit Input:

Contents of protection data in fixup vector

Implicit Output:

Pages in address ranges specified in fixup vector have their protections
 changed to the protections also specified in that data area. The
 protection is usually no write access for any access mode.

Side Effects:

R0, R1, and R2 are destroyed

FIXUP_PROT:

5E 56	DD	0138	575	PUSHL	R6	: Need one more register here	
14	C2	013A	576	SUBL2	#<4*SETPRTS_NARGS>,SP	: Set up space for argument list	
05	DD	013D	577	PUSHL	#SETPRTS_NARGS	: Push argument count	
56	5E	D0	013F	578	MOVL	SP,R6	: Use R6 as argument pointer
7E		7C	0142	579	CLRQ	-(>SP)	: Initialize input address array
04 A6	5E	D0	0144	580	MOVL	SP,SETPRTS_INADR(R6)	: Put its address into argument list
08	A6	D4	0148	581	CLRL	SETPRTS_RETADR(R6)	: Not interested in this argument
0C A6	01	D0	014B	582	MOVL	#PSLSC_EXEC,SETPRTS_ACMODE(R6)	: The image activator owns these page
14	A6	D4	014F	583	CLRL	SETPRTS_PRVPRT(R6)	: Not interested in this either
50 0000'8F	3C	0152	584	MOVZWL	#SSS_NORMAL,R0	: Establish initial status	
52 84	D0	0157	585	MOVL	(R4)+,R2	: Get count of number of protection changes	
20	13	015A	586	BEQL	20\$: Do not even start if nothing here	
6E 84	53	C1	015C	587 10\$:	ADDL3	R3,(R4)+,(SP)	: Get starting address
51 84	3C	0160	588	MOVZWL	(R4)+,R1	: Ending address must be calculated	
51 09	78	0163	589	ASHL	#9,R1,R1	: ... from page count in image section	
51	D7	0167	590	DECL	R1	: Make byte count an inclusive count	
04 AE	51	C1	0169	591	ADDL3	R1,(SP),4(SP)	: Put ending address in second longword
10 A6	84	3C	016E	592	MOVZWL	(R4)+,SETPRTS_PROT(R6)	: Get new protection from fixup vector
00000000'GF	66	FA	0172	593	CALLG	(R6),G^SYSSSETPRT	: Call the system service
E0 52	F5	0179	594			: Ignore errors	
SE 20	C0	017C	595 20\$:	SOBGTR	R2,10\$: Go get next image section	
			596	ADDL2	#<8+4+<4*SETPRTS_NARGS>>,SP ;Reset stack pointer,		

SY\$IMGFIX
V04-000

- Address Fixup System Service L 1
FIXUP_PROT Alter page protection to read 16-SEP-1984 02:20:23 VAX/VMS Macro V04-00
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SYS
V04

56 8ED0 017F \$98
05 0182 \$99

POPL R6
RSB

; restore that extra register,
; and return

0183 601 .SBTTL IMGSPRVSHRIMG Fixup Routine for Privileged Shareable Images
 0183 602 :+
 0183 603 Functional Description:
 0183 604 This routine checks that a privileged shareable image has no
 0183 605 outbound calls. For images passing this test, remaining
 0183 606 .ADDRESS fixups are performed.
 0183 607
 0183 608
 0183 609
 0183 610
 0183 611
 0183 612
 0183 613
 0183 614 Calling Sequence:
 0183 615 BSBW IMGSPRVSHRIMG
 0183 616 Input Parameters:
 0183 617 R0 Address of fixup vector
 0183 618 R1 Base address of privileged shareable image currently
 0183 619 being mapped
 0183 620 Implicit Output:
 0183 621 If the fixup vector indicates no outbound calls, the base address
 0183 622 of the privileged shareable image is stored in the fixup vector
 0183 623 and the .ADDRESS fixups are performed.
 0183 624
 0183 625 Side Effects:
 0183 626 R0 and R1 are destroyed
 0183 627
 0183 628 Completion Codes:
 0183 629
 0183 630 SSS_NORMAL Fixups were completed for privileged shareable image
 0183 631
 0183 632 SSS_NOSHRIMG Shareable image has outbound calls
 0183 633
 0183 634
 0183 635
 0183 636 IMGSPRVSHRIMG::
 50 1C A5 3C 0183 637 PUSHR #^M<R2,R3,R4,R5> : Save some registers
 50 55 50 D0 0183 638 MOVL R0,R5 : Store fixup vector address in R5
 50 01 C3 0183 639 SUBL3 #1 IAFSL_SHRIMGCNT(R5),R0 : Is shareable image count 1?
 50 28 12 0183 640 BNEQ 30\$: If not, report error
 50 A5 D5 0183 641 TSTL IAFSL_G_FIXOFF(R5) : Also report error if G* fixup data
 50 26 12 0183 642 BNEQ 30\$
 50 53 51 D0 0183 643 MOVL R1,R3 : Store base address of image in R3
 50 55 C1 0183 644 ADDL3 R5,IAFSL_SHLSTOFF(R5),R0 : Also store base address in
 50 60 51 D0 0183 645 MOVL R1,SHLSL-BASEVA(R0) : SHL entry for SHIMG_BASVA
 54 10 A5 D0 0183 646 MOVL IAFSL_DOTADROFF(R5),R4 : Any .ADDRESS fixups?
 54 06 13 01A3 647 BEQL 10\$: Branch if none
 54 55 C0 01A5 648 ADDL2 R5,R4 : Convert R4 offset to address
 54 FF71 30 01AB 649 BSBW FIXUP_ADDRESS : Fixup all .ADDRESS data
 54 14 A5 D0 01AB 650 10\$: MOVL IAFSL_CHGPRTOFF(R5),R4 : Get offset to protection data
 54 06 13 01AF 651 BEQL 20\$: All done if none
 54 55 C0 01B1 652 ADDL2 R5,R4 : Make R4 an address
 54 FF81 30 01B4 653 BSBW FIXUP_PROT : Change page protection
 50 3C BA 0187 654 20\$: POPR #^M<R2,R3,R4,R5> : Restore registers
 50 05 0189 655 RSB and return
 50 01BA 656
 50 0000'BF 3C 01BA 657 30\$: MOVZWL #SSS_NOSHRIMG,R0 : No outbound calls allowed

SYSSIMGFIX
V04-000

- Address Fixup System Service N 1
IMGSPRVSHRIMG Fixup Routine for Privileg 16-SEP-1984 02:20:23 VAX/VMS Macro V04-00
5-SEP-1984 03:54:43 [SYS.SRC]SYSIMGFIX.MAR;1

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F6 11 01BF 658 BRB 208

; Return error status

SYS
V04

01C1 660 .SBTTL INISHRIMG - Look for and Call Shareable Image Initialization Code
 01C1 661 :+
 01C1 662 Functional Description:
 01C1 663
 01C1 664 This routine searches the shareable image list for images that have
 01C1 665 included initialization code.
 01C1 666
 01C1 667 Calling Sequence:
 01C1 668
 01C1 669 BSBW INISHRIMG
 01C1 670
 01C1 671 Input Parameters:
 01C1 672 none
 01C1 673
 01C1 674
 01C1 675 Implicit Input:
 01C1 676
 01C1 677 IAC\$GL_IMAGE_LIST - List of ICBs describing shareable images that
 01C1 678 are currently mapped.
 01C1 679 IAC\$GL_FIRST_ICB - Address of ICB representing main image in the
 01C1 680 most recent image activation.
 01C1 681
 01C1 682 Implicit Output:
 01C1 683
 01C1 684 If there are any images with ICBs containing shareable image
 01C1 685 initialization code, these procedures are called at their entry
 01C1 686 points. Note that the ICB list is traversed backwards.
 01C1 687
 01C1 688 Side Effects:
 01C1 689
 01C1 690 R0 and R1 are destroyed
 01C1 691
 01C1 692 Completion Codes:
 01C1 693
 01C1 694 none
 01C1 695 :-
 01C1 696
 01C1 697 INISHRIMG:
 52 7E 52 7D 01C1 698 MOVQ R2-(SP) : Save some registers
 53 00000000'GF DE 01C4 699 MOVAL G^IAC\$GL_IMAGE_LIST,R2 : Get the listhead address
 00000000'GF DO 01CB 700 MOVL G^IAC\$GL_FIRST_ICB,R3 : This is the stopper
 01D2 701
 52 04 A2 D0 01D2 702 10\$: MOVL ICB\$L_BLINK(R2),R2 : Get the next ICB
 05 E1 01D6 703 BBC #ICBS\$ INITIALIZE- : Does this image need to be called?
 09 10 A2 01D8 704 ICB\$L_FLAGS(R2),20\$: Branch if no initialization routine
 51 60 A2 C1 01DB 705 ADDL3 ICB\$L_INITIALIZE(R2),- : Form the address of the entry point
 61 00 FB 01E1 706 ICB\$L_BASE_ADDRESS(R2),R1 :
 53 52 D1 01E4 708 20\$: CALLS #0,(RT) : Call the routine
 E9 12 01E7 709 CMPL R2,R3 : Is this the end of the line?
 52 8E 7D 01E9 710 BNEQ 10\$: Back to the top if there's more
 C5 01EC 711 MOVQ (SP)+,R2 : Restore R2 and R3
 01ED 712 RSB : All done. Return to caller.
 01ED 713 .END

SSARGS
 \$ST1
 CTL\$GL FIXUPLNK
 EXESIMGFI
 FIXUP_ADDRESS
 FIXUP_G_HAT
 FIXUP_PROT
 GET_BASE_ADDRESSES
 IAC\$GL_FIRST_ICB
 IAC\$GL_IMAGCTX
 IAC\$GL_IMAGE_LIST
 IAC\$M_SETVECTOR
 IAF\$L_CHGPRTOFF
 IAF\$L_DOTADROFF
 IAF\$L_FIXUPLNK
 IAF\$L_G_FIXOFF
 IAF\$L_SALSTOFF
 IAF\$L_SHRIMGCNT
 ICBSL_BASE_ADDRESS
 ICBSL_BLINK
 ICBSL_FLAGS
 ICBSL_FLINK
 ICBSL_INITIALIZE
 ICBS\$T_IMAGE_NAME
 ICBSV_INITIALIZE
 IMAGCTX\$V_INITIALIZE
 IMAGCTX\$V_SETVECTOR
 IMG\$IS_IT_MAPPED
 IMG\$PRVSHRIMG
 IMG\$ IMAGE_NOT_FOUND
 INISHRIMG
 PROCESS_FIXUP_LIST
 PSL\$C_EXEC
 SETPRT\$_ACMODE
 SETPRT\$_INADR
 SETPRT\$_NARGS
 SETPRT\$_PROT
 SETPRT\$_PRVPRT
 SETPRT\$_RETADR
 SHIMG_BASVA
 SHL\$B_SHL_SIZE
 SHL\$L_BASEVA
 SHL\$T_IMGNAM
 SSS_NORMAL
 SSS_NOSHRIMG
 SYSSIMGACT
 SYSSSETPRT

+-----+
! Psect synopsis !
+-----+

PSECT name

Allocation PSECT No. Attributes

PSECT name	Allocation	PSECT No.	Attributes
ABS .	00000000 (0.)	00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
SABSS	00000000 (0.)	01 (1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
YF\$SYSIMGACT	000001ED (493.)	02 (2.)	NOPIC USR CON REL LCL NOSHR EXE RD WRT NOVEC BYTE

+-----+
! Performance information !
+-----+

Phase	Page faults	CPU Time	Elapsed Time
Initialization	34	00:00:00.07	00:00:00.40
Command processing	136	00:00:00.73	00:00:03.65
Pass 1	192	00:00:04.03	00:00:10.67
Symbol table sort	0	00:00:00.26	00:00:00.34
Pass 2	133	00:00:01.52	00:00:03.66
Symbol table output	7	00:00:00.05	00:00:00.05
Psect synopsis output	2	00:00:00.03	00:00:00.04
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	506	00:00:06.69	00:00:18.82

The working set limit was 1500 pages.

21961 bytes (43 pages) of virtual memory were used to buffer the intermediate code.

There were 20 pages of symbol table space allocated to hold 218 non-local and 25 local symbols.

713 source lines were read in Pass 1, producing 14 object records in Pass 2.

20 pages of virtual memory were used to define 19 macros.

+-----+
! Macro library statistics !
+-----+

Macro library name	Macros defined
\$255\$DUA28:[SYS.OBJ]IMGACT.MLB;1	3
\$255\$DUA28:[SYS.OBJ]LIB.MLB;1	2
\$255\$DUA28:[SYSLIB]STARLET.MLB;2	11
TOTALS (all libraries)	16

329 GETS were required to define 16 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LISS:SYSIMGFI X/OBJ=OBJ\$:SYSIMGFI X MSRC\$:SYSIMGFI X/UPDATE=(ENHS:SYSIMGFI X)+EXECMLS/LIB+LIBS:IMGACT/LIB

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SYSARAM
LIS

SYSMTACC
LIS

SYSLOGNAM
LIS

SYSLOAVEC
LIS

SYSIMGSTA
LIS

SYSLNMM
LIS

SYSLKSET
LIS

SYSMAILBX
LIS